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APPLICANTS
 Minari Arai, Tokyo, JAPAN;

** CONTINUING DATA *None*

** FOREIGN APPLICATIONS *Yes*
 JAPAN 2002-354993 12/06/2002

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials <i>YJ</i>				

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TITLE
 Semiconductor integrated circuit device and method of detecting delay error in the same

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